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Application Number	09/057,861
Filing Date	April 9, 1998
First Named Inventor	Sachs, Howard
Group Art Unit	2758-2154
Examiner Name	V. Vu

Attorney Docket Number 012172-004530US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
W.	AA	5,355,460		Eickemeyer et al.	10/11/94	
W.	AB	5,459,844		Eickemeyer et al.	10/17/95	

FOREIGN PATENT DOCUMENTS

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
✓v.	AD	Horst et al., "Multiple instruction issue in the nonstop cyclone processor" Proceedings of the 17th Annual International Symposium on Computer Architecture, IEEE Computer Society Press, Washington, (May 28-31, 1990) pp. 216-226.	
vv.	AE	Requa et al., "The piecewise data flow architecture: Architectural concepts" IEEE Transactions on Computers (1983) C-32(5):425-438.	
vv.	AF	Wang et al., "I-NET mechanism for issuing multiple instructions" Proceedings of Supercomputing" IEEE Computer Society (November 14-18, 1988) Orlando, Florida, pp. 88-95.	

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¹ Unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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